

Curriculum Vitae

Ziaddin Daie Koozehkanani, Ph.D.

Full Professor

Department of Electronics,
Faculty of Electrical and Computer Engineering
University of Tabriz, Tabriz, IRAN

Office: +98 (411) 3393743 and 3393709; Fax: +98 (411) 3300819

Email: zdaie@tabrizu.ac.ir

ziadaie@yahoo.com

URL1: <http://asatid.tabrizu.ac.ir/en/pages/default.aspx?zdaeie>

Last Updated: 25-May-2014

Personal Data:

Name: Ziaddin

Surname : Daie Koozehkanani

Date of Birth:18,May,1959

Nationality Iranian

Sex:Male

Telephone: (+98)4113393743,(+98)4113393709

Fax: (+98)4113300819

E-mail: zdaie@tabrizu.ac.ir and ziadaie@yahoo.com

Postal Address: Department of Electronics, Faculty of Electrical and Computer Engineering, University of Tabriz, Tabriz, Iran

Educational Background:

Ph.D. Degree in Field of **Active Filters** from **Univercity of Brunel**,London,U.K. on 1996.

M.Sc. Degree in Field of **Electronics** from **Univercity of Tehran**, Tehran, IRAN on 1985.

B.Sc. Degree in Field of **Electronics** from **Univercity of Tehran**, Tehran, IRAN on 1983.

Title of MSc Thesis: Wideband Logarithmic Periodic Antennas

Title of PhD Thesis: A Unified Approach on Active Filters

Academic Positions:

Full-time Associate Profeesot at the Department of Electronics, Electrical and Computer Engineering Faculty,University of Tabriz, Tabriz, Iran, (2011-present)

Administrative Responsibilities:

Head of Electronics Department, University of Urmia, IRAN, 1989-1991

Head of Electronics Department, University of Tabriz, IRAN, 2005-2006

Head of Electronics Department, University of Tabriz, IRAN, 2008-2009

Dean of Electrical and Computer Engineering Faculty, University of Tabriz, IRAN, 2009-2014,2016 up to 2021

Head of innovation and incubation technology center, University of Tabriz. 2014-2016

Teaching Experiences:

ADC and DAC converters- Low Noise Amplifiers - Switched Capacitor Circuits Design - Analog MOS Integrated Circuits - Reconfigurable SOC on MS.c and Ph.d. Level

Electronics 1,2,3-Signals and Systems-Engineering Mathematics-Communication Circuits on B.Sc.

Selected Papers in indexed international journals:

- 1) ALL Optical Tunable Mirror Design Using Electromagnetically Induced Transparency
K Abbassian, A Rostami, Z D Koozehkanani
PIER M,2008
- 2) A OSCI TLM-2.0 based Platform for Grid Computing Simulation
M Zolfy, Z D Koozehkanani Z Navabi,
International Journal of Computer Application ISSN: 0975-8887
- 3) Active filter for Eliminating Current Harmonics Caused by Nonlinear Loads
Z D Koozehkanani, P Mehta
Electronics Letters,1995,ISI
- 4) A very high speed CMOS 4-2 compressor using fully differential current-mode circuit techniques
Peiman Aliparast, Ziaddin.Daie.Koozehkanani ,Abdolhamid Moalemi,Ghader Karimian,Hossein Balazadeh Bahar
Analog Integr Circ Sig Process , 2011
- 5) A modified Approach for CMOS Auto – Zeroed Offset –Stabilized Opamp
Abozar Taghizadeh, Ziaddin.Daie.Koozehkanani,Jafar Sobhi
Circuit and Systems,2013
- 6) All optical multi reflection structure based on one dimensional photonic crystals
Farhad Mehdizadeh, Hamed Alipour Banaie, Ziaddin.Daie.Koozehkanani,
Circuit and Systems,2013
- 7) A new current mode APS in 0.18um standard CMOS process for smart image sensors
Peiman Aliparast, Ziaddin.Daie.Koozehkanani
Analog Integr Circ Sig Process , 2013
- 8) A wideband multipath CMOS OTA for high speed application
Ali Vatanjou, Ziaddin.Daie.Koozehkanani,Jafar Sobhi,and Farhad Nobakht Daghdaghan
ELEX, 2011
- 9) Ultra low power frequency divider for 2.45GHz ZigBee frequency synthesizer
Ali Sahafi,Jafar Sobhi,Mahdi Sahafi,Omid Farhanieh,And Ziaddin Daie Koozehkanani
Springer , 2012
- 10) Nano Watt CMOS temperature sensor
Ali Sahafi,Jafar Sobhi,,And Ziaddin Daie Koozehkanani
Analog Integr Circ Sig Process , 2013
- 11)Zero –Dispersion shifted optical Fiber Design Based on GA and CD Optimised Method
S.Makouie and Z D Koozehkanani
Pier 2012
- 12)Zero –Dispersion shifted optical Fiber Design Based on GA and CD Optimised Method
S.Makouie and Z D Koozehkanani
Pier 2012
- 13) Design of A 12bit 1 high speed CMOS D/A converter using a new 3D decoder structure useful for wireless transmitter application
P Aliparast, ,Z D Koozehkanani, J.Sobhi
Analog Integr Circ Sig Process , 2011
- 14) A novel Proposal For Ultra-High Resolution And Compact Optical Displacement Sensor
R Yadipour, K Abbasian, A Rostami, Z D Koozehkanani
PIER, 2007,ISI
- 15) A Wideband Multipath CMOS OTA for High Speed Application
A Vatanjou, Z D Koozehkanani, J Sobhi
ELEX,Electronics Express,2011 ISSN:1349-2543, ISI
- 16) A 90nm CMOS +11dbm IIP3 4mW Dual band for Cellular Handsets
G Z Fatin, Z D Koozehkanani, H Sjouland
IEEE MWCL Microwave and Wireless Components letters, 2010 ISSN: 1531-1309,ISI
- 17) A Very Low Power Bandpass Filter For Low IF Application
G Z Fatin, Z D Koozehkanani
Journal of Circuits, Systems, and Computers, 2008,ISI
- 18) A Technique for Improving Gain and Noise Figure of Common-Gate Wideband LNAs
G Z Fatin, Z D Koozehkanani, H Sjouland
Analog Integrated Circuits and Signal Process, 2010 ISSN: 0925-1030
- 19) A Very High-speed CMOS 4-2 Compressor using fully Differential Current-Mode Circuit Techniques
P Aliparast, Z D Koozehkanani, A Moalemi, G Karimian, H Balazadeh
Analog Integrated Circuits and Signal Process, 2011 ISSN:0925-1030,ISI

- 20) A 12bit 1 Gsample/s Nyquist Current-Steering DAC in 0.35 micrometer CMOS Wireless Transmitter
P Aliparast, H B Bahar, Z D Koozehkanani, J. Sobhi, G Karimian
Journals of circuits and Systems, 2011 ISSN:2153-1293, ISI
- 21) Proposal for Optical Fiber design with Ultrahigh Effective Area and Small Bending Loss Applicable to Long Haul Communication
M.S Oskkouie S Makouie A. Rosami Z.D. Koozehkanani
APPLIED OPTICS, 2006, ISI
- 22) Dispersion Flattened Optical Fiber Design for Large Bandwidth and High-Speed Optical Communication
S Makouie, M S Oskkouie, A Rostami, Z D Koozehkanani
PIER B, 2009
- 23) Design of pseudo-symmetric high bit rate, bend insensitive optical fiber applicable for high speed FTTH
Somayeh Makouie, Z D Koozehkanani
ELSEVIER, Optic Communication, 2014
- 24) High Resolution High Sampling Rate Sine Wave Generator Based on Modified Taylor Series
A Moalemi, J Sobhi, Z D Koozehkanani
Electronic Information and Planning B, 2015
- 25) Dispersion Flattened Optical Fiber Design for Large Bandwidth and High-Speed Optical Communication
H Jangi Bahador, Z D Koozehkanani, H Balazadeh, J Sobhi,
IETE Journal of Research, 2015
- 26) Design of efficient power amplifier for low power transmitters, M Modava, A Sahafi, J Sobhi, ZD Koozehkanani - Analog Integrated Circuits and Signal Processing, 2017.
- 27) Design of a reconfigurable front-end for a multistandard receiver for the frequency range of 800 MHz to 2.5 GHz, G Zare Fatin, ZD Koozehkanani, A Fotowat-Ahmady - International Journal of Circuit Theory and Applications, 2018.
- 28) A 910MHz Injection Locked BFSK Transceiver for Wireless Body Sensor Network Using Colpitts Oscillator M Yousefi, ZD Koozehkanani, J Sobhi - Journal of Iranian Association of Electrical and Electronics Engineers, 2018.
- 29) FPGA-Based Reconfigurable PWM Generator for Power Electronic Converter Applications, AM Khiavi, J Sobhi, ZD Koozehkanani, MF Kangarlu - Journal of Control, Automation and Electrical Systems, 2017.
- 30) Very linear open-loop CMOS sample-and-hold structure for high precision and high speed ADCs
TM Khanshan, M Nematzade, K Hadidi, A Khoei, ZD Koozehkanani - Analog Integrated Circuits and Signal Processing, 2016.
- 31) Linearity improvement of gm-booster common gate LNA: Analysis to design, A Sahafi, J Sobhi, ZD Koozehkanani - Microelectronics Journal, 2016.
- 32) A new high-speed low-power and low-offset dynamic comparator with a current-mode offset compensation technique, A Taghizadeh, ZD Koozehkanani, J Sobhi - AEU-International Journal of Electronics, 2017.
- 33) A- 10 dBm 5 Mbps Energy-Efficient Injection-Locked FSK Transceiver for Wireless Body Sensor Networks, HJ Bahador, ZD Koozehkanani, HB Bahar, J Sobhi - IETE Journal of Research, 2016.
- 34) A PMSM current controller system on FPGA platform, S Ahmadian, AM Khiavi, J Sobhi, ZD Koozehkanani - Journal of Fundamental and Applied Sciences, 2016.
- 35) A sub-mW 2.9-dB noise figure Inductor-less low noise amplifier for wireless sensor network applications
J Sobhi, ZD Koozehkanani - AEU-International Journal of Electronics, 2018.
- 36) A linear, low power, 2.5-dB NF LNA for UWB application in a 0.18 μm CMOS, I Mohammadi, A Sahafi, J Sobhi, ZD Koozehkanani - Microelectronics Journal, 2015.